

प्रा. मनिष र. जोशी सचिव

Prof. Manish R. Joshi

Secretary





विश्वविद्यालय अनुदान आयोग University Grants Commission

(शिक्षा मंत्रालय, भारत सरकार) (Ministry of Education, Govt. of India)

D.O. No. 5-13/2023 (NSQF/NIELIT)

0 3 APR 2023 April, 2023

Subject: Skilled Manpower - Advanced Research and Training (SMART) facility set up at NIELIT, Calicut as part of Chip to Start-up (C2S) programme- Regarding.

Dear Madam/Sir.

As you may be aware, the National Institute of Electronics and Information Technology (NIELIT), Calicut center has set up a Skilled Manpower Advanced Research and Training (SMART) facility or Virtual Prototyping Lab as part of Chip to Start-up (C2S) programme of Ministry of Electronics and Information Technology for the proliferation of advanced VLSI and Embedded system design training, search and electronics systems development across the country.

- · This facility at NIELIT, Calicut is on free of cost basis.
- The 'SMART' remote lab facility is available 24x7 and the students, researchers, and start-up
 industries can access the facility, anytime and anywhere.
- To learn electronics hardware and embedded system design concepts and to acquire design skills
 it is inevitable to practice laboratory experiments. The facility will enable the generation of skilled
 manpower as well as, Intellectual Property generation in VLSI, electronics hardware and
 embedded system design the EDA Tools, test & measuring equipment like logic analyzers,
 spectrum analyzers, digital storage oscilloscopes etc.
- As per the Semicon India Future skills Talent Committee Repot Nov 2022, in India needs 3.3 lakh skilled manpower in electronic design by 2030 and 4.5 lakhs by 2032. The SMART lab facility at NIELIT Calicut will be an enabler for the talent roadmap.
- Through the SMART lab facility, NIELIT is offering MOOC Courses in VLSI and Embedded domain of 65 hours duration (3 credits) as well as 1-4 months academic projects and internships

You are requested to encourage your students and faculties to utilize the SMART facility at NIELIT, Calicut on free of cost. A copy of brochure along with some important highlights of the SMART lab project is enclosed as **Annexure-1**.

Yours sincerely,

(Manish Joshi)

Encls.: As above

- 1. Vice-Chancellor of Identified Universities
- 2. Principal of Identified Colleges.